

NVMTS 2008 CALL FOR ABSTRACTS

The 9th Annual Non-Volatile Memory Technology Symposium, will be held this year at the Asilomar Conference Grounds in Pacific Grove, California, **November 11-14, 2008**. We encourage you to submit an abstract for the conference describing your research and advances in new and alternative non-volatile memory technologies. Please see the conference website <http://nvmts.org> for more information.

Abstract Submission:

The deadline for abstract submission is August 1, 2008. Authors who submit by the deadline will be notified of paper acceptance by August 21, 2008. Submissions after August 1st will still be considered if space is available. Please see the conference website for additional information: http://nvmts.org/Abstract_Submission.htm. There are awards available for ten students to receive a registration fee waiver. If you are a student, please contact Dr. Jeff Peloquin (jeffreypeloquin@boisestate.edu) if you are interested in this award.

Lodging:

The conference will be held at the Asilomar Conference Grounds, P O Box 537, 800 Asilomar Blvd., Pacific Grove, CA 93950. Information on room reservation is available at: http://nvmts.org/Site_Info.htm.

Important Deadlines

Abstract Submission	Aug. 1st
Final Paper Submission	Oct. 10th
Early Conference Registration	Sept. 10th
Room Reservations	Sept. 10th

This year we will have two new events in addition to the standard program:

- [A Tutorial](#) for students, post-docs and anyone else looking for an overview of the Non-Volatile Memory Technologies. This tutorial will be led by Dr. Krishna Parat from Intel and will be held from 4:00 pm to 6:00 pm on Nov 11th (the first day of the symposium).
- A **Poster session** for students, post-docs and researchers to present and discuss their work.

Keynote Speaker:

We are very pleased this year to have **Dr. Stephen J. Hudgens** from **Ovonyx, Inc.** as our keynote speaker. Dr. Hudgens has been active in the non-volatile memory field for over 20 years, is the holder of numerous patents focused on the design of new non-volatile memory cells and has published extensively on the means by which the performance of non-volatile memory devices can be improved.

Technical Sessions:

- Phase change memory
- Metal oxide memory
- Ion conducting memory
- Ferroelectric
- New memory concepts
- Flash
- Magnetic RAM
- NVM in reconfigurable electronics
- Memory circuit design and integration

For more information, contact Dr. Kris Campbell, general chair (kriscampbell@boisestate.edu), or Dr. Jeff Peloquin, Publications/Finance Chair (jeffreypeloquin@boisestate.edu).